

### Amendments to the Specification

**Please replace the paragraph beginning on page 5, line 20 with the following amended paragraph:**

A1  
In this level shift circuit, the output of the inverter 1215 is at high level when the input signal IN is at low level. Therefore the nMOS transistor 1213 is OFF, and the nMOS transistor 1214 is ON. Since the nMOS transistor 1214 is ON, the potential of the node N2, that is, the signal level of the output signal OUT, is at low level. As a result, the pMOS transistor 1211 is ON, therefore the potential of the node ~~[[N2]]~~ N1 is at high level. This means that the pMOS transistor 1212 is OFF.

**Please replace the abstract with the following amended abstract:**

A2  
~~A level shift circuit whereby a voltage shift amount is large, operation speed is fast, and the power consumption is low. A p-type~~ including a first transistor ~~[[is]]~~ connected between ~~[[the]]~~ a power supply line and ~~[[the]]~~ a first node, a p-type second transistor ~~[[is]]~~ connected between the power supply line and ~~[[the]]~~ a second node, and an n-type a third transistor ~~[[is]]~~ connected between ~~[[the]]~~ a ground line and the first node, and an n-type a fourth transistor ~~[[is]]~~ connected between the ground line and the second node. ~~The gate~~ Gates of the first and second transistors are respectively transistor is connected to the second node~~[[,]]~~ and the gate of the second transistor is connected to the first node. An input signal is supplied to ~~[[the]]~~ a gate of the third transistor and an ~~inverted value of the input signal~~ inverted is supplied to ~~[[the]]~~ a gate

A<sup>2</sup>  
of the fourth transistor. ~~Additionally, this level shift circuit has a plurality of control~~  
~~transistors. The A~~ control transistor switches ~~[[the]]~~ a ratio of ~~[[the]]~~ inflow current and  
emission current of the first ~~[[node]]~~ or ~~[[the]]~~ second ~~[[node]]~~ nodes according to ~~[[the]]~~  
a control signal. The operation speed increases if this ratio is set high, and the voltage  
shift amount increases if this ratio is set low.

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